



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Cho et al. Serial No.: 09/923,670

Filed: August 7, 2001

Group Art Unit: 2836 Examiner: M. Lewis

Confirmation No.: 1538

INTEGRATED CIRCUIT DEVICES HAVING DIELECTRIC REGIONS PROTECTED

WITH MULTI-LAYER INSULATION STRUCTURES AND METHODS OF

**FABRICATING SAME** 

Date: November 5, 2002

**BOX NON-FEE AMENDMENT** Commissioner for Patents Washington, DC 20231

## RESPONSE TO ELECTION OF SPECIES REQUIREMENT

Sir:

This paper is responsive to the Office Action of October 21, 2002.

## REMARKS

In response to the election of species requirement in the Office Action of October 21, 2002, Applicants hereby provisionally elects the species of FIGs. 1A, 1B, 2A-2C, and 3A-3C with traverse, and requests examination of Claims 58-72, which read thereon. Applicants submit that at least Claims 42 and 58 are generic to all illustrated method embodiments. Accordingly, Applicants further requests examination and allowance of all the pending claims upon allowance of a generic claim. Applicant further requests examination and allowance of all the pending claims upon allowance of a generic claim.

Applicants also submit herewith a copy of reference KR10-1998-0036879, as requested in the

Office Action.

Respectfully submitted

Robert M. Meeks Registration No. 40,723

Attorney for Applicant(s)

PATENT TRADEMARK OFFICE

## **CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: BOX NON-FEE AMENDMENT, Commissioner for Patents, Washington, DC 20231, on July 8,

andi L. Riggs

Date of Signature: July 8, 2002

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